

OCT 12 2006

566.32253CC8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. KAKI et al.
Serial No.: 10/784,995
Filed: February 25, 2004
Title: SEMICONDUCTOR STORAGE APPARATUS IN WHICH
DIFFERENT ERASE COMMANDS ARE SEQUENTIALLY SENT
TO A PLURALTY OF NONVOLATILE SEMICONDUCTOR
MEMORIES
Group: 2189
Examiner: ROJAS, Midys
Confirmation No.: 9779

REQUEST FOR RECONSIDERATION AND REQUEST FOR INTERVIEW

Mail Stop: AF
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

October 12, 2006

Sir:

In response to the Final Office Action dated July 12, 2006, reconsideration and allowance of this application is respectfully requested. In addition, following review of the following comments, it is requested that the Examiner contact the undersigned attorney for purposes of conducting an interview in this matter.

Briefly, the present invention is directed to solving specific problems in slow processing speed with regard to non-volatile semiconductor memories. In order to compensate for such slow processing and non-volatile memories, the present independent claims 1 and 16 both define the feature that:

"Said control module sends a second erase command to another of said plurality of semiconductor memories, different from said one of said plurality of non-volatile semiconductor memories to which said first erase command was sent, to